

6.(Amended) A neurocomputer comprising:

a plurality of n processing element means;

a plurality of no more than n connectors operably coupled with said
element means;

means for simultaneously applying an oscillatory signal to each of said
element means via said connectors; and

means for generating said oscillatory signal operably coupled with said
means for applying.

7.(Amended) The neurocomputer of claim 6, wherein:

each of said connectors is operably coupled with a corresponding one of said element
means.

Please cancel claim 13 without prejudice.

Please cancel claim 14 without prejudice.

25.(Amended) In a neurocomputer, a number n of active elements and a medium having no
more than n connections operably coupled to each of the active elements for application of an
input signal thereto, said active elements being phase locked loop oscillators.

REMARKS

Claims 5-12 and 15-25 remain in the application. By this amendment claims 6, 7, and
25 have been amended and claims 1-4, 13, and 14 have been canceled without prejudice.

The specification has been amended to correct various typographical and grammatical
errors contained therein.

Attached hereto is a marked-up version of the changes made to the specification and
claims by the current amendment. The attached page is captioned **“Version with markings to
show changes made.”**